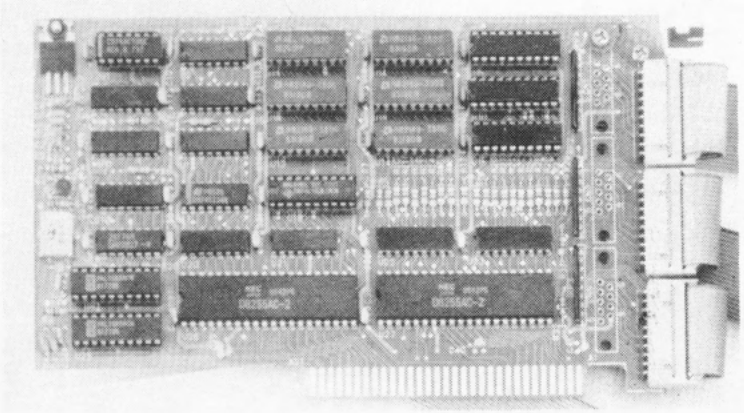
Design Science

PERSONAL LOGIC ANALYZER

MODEL PL/A 524



FEATURES

* High performance: 50 MHz, 24 channels
* Easy to use
* Low cost
* Timing display with 2 movable cursors & zoom
* State display with ASCII, binary, decimal & hex
* User defined channel labels
* 24-bit trigger word with automatic re-trigger
* Disk storage of setups and traces
* Outputs timing and state displays to printer
* All 24 channels available at full rate
* Adjustable pre/post trigger

APPLICATIONS

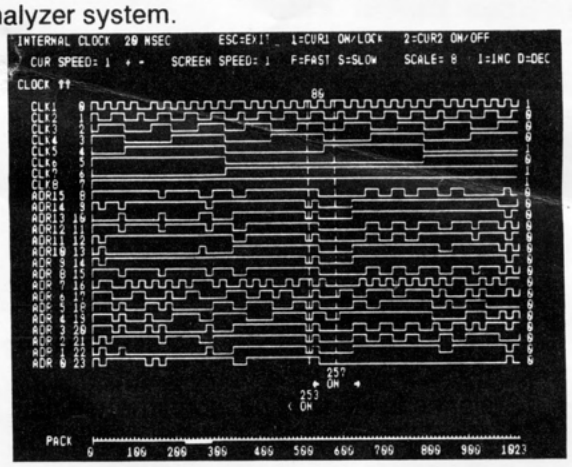
Ideal for circuit design and debugging, service technicians, and production line testing.

DESCRIPTION

The PL/A 524 Personal Logic Analyzer is a complete logic analyzer on a PC plug in board. It provides 24 channels of state and timing analysis at speeds up to 50 million samples per second, and 1024 samples per trace. Menu driven software is included which allows you to easily and quickly capture, view, and save the data to printer or disk.

The PL/A approach offers the ideal combination of performance, reliability, and ease of use by turning your PC into a full-featured analyzer comparable to units costing thousands more. The large screen, standard ASCII keyboard, and menu-driven software all combine to create an easy to learn and use system. And of course you can easily add printers, hard disks, and other laboratory hardware/software to create a truly versatile system. In addition, the PL/A 524 generates standard MS-DOS files which can be ported to other systems, and processed by other PC software. You can even make your own low-cost automated test software.

The PL/A works with all IBM PC/XT/AT and compatible computers, including portables. It is also available with our space-saving 286 Laptop with high-res display, and optional 20MB hard disk, which provides a complete portable self-contained analyzer system.



SYSTEM REQUIREMENTS

Works with any IBM PC/XT/AT or compatible
CGA, Hercules, or EGA display
MS-DOS 2.0 or higher
512k RAM

OTHER PRODUCTS

- 286 Laptop
- 200MHz logic analyzer
- Optical disc analyzers
- Digital Audio Workstations



SOFTWARE COMMANDS

Setup

Set clock rate
Select internal or external clock
Select clock trigger edge
Select pre-trigger buffer
Select trigger word
Assign names to channels
Map physical channels to logical channels

Capture Data

Capture
Capture on trigger word
Repeat on trigger
Repeat each 1sec.

State Display

Display ASCII, hex, decimal and binary
Scroll thru buffer
Jump to event

Timing Display

All 24 channels displayed on screen at once
Scroll forward & backward in buffer
On-screen visual indicator of current position in buffer
Two movable cursors
Zoom horizontal scale 1x to 64x
User programable sequence of channels in display
Time between cursors A and B is displayed

Files

Save data with setup parameters
Load data with setup parameters
View directory
Delete, rename files
Change path

Printer

Hardcopy of timing diagrams, setup, & state display
Timing display magnification: 1, 2, 4, 8, 16, 32, 64 x

SPECIFICATIONS

TTL input trigger levels (1.4v)
Internal clock: 50MHz, 5MHz, 1MHz, 500kHz, 100kHz, 50kHz, 25kHz
External clock: 0 - 50MHz
Timing skew: 2nS max.
Input impedance: 1 Mohm, < 15pF
Maximum input voltage: +/- 25 V
Setup time: Internal - 4 nS
External - 0 nS
Hold time: Internal - 0 nS
External - 10 nS

Trigger setup time: 40 nS
Trigger qualifiers: 24 bits; 1, 0, don't care
Trigger edge: rising or falling(external)
Pre-trigger buffer: 15 settings; 64 to 960 samples
User label: up to 5 characters
Power Requirement: 1.5 A @ 5 volts
Board dimensions: 7" x 4.25"
Shipping weight: 3 lbs.

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